



# Gandhinagar Institute of Technology

A Report on

“Alumni Talk Series - Abhivyakti”

Webinar conducted by Mr Akash Verma

(Latest Advancements in AI, ML, VLSI & Automotive  
Realm)

(14<sup>th</sup> June 2020, Sunday)

## Alumni Talk at a Glance:

- Date: 14-06-2020, Time: 11:00 AM, Platform: Google Meet
- Invited Alumnus: Mr. Akash Verma,  
ASIC Design Engineer,  
Facebook.
- Passout Year: 2018 EC dept.
- Host: Prof Vineeta Nishad & Moderator: Prof Megha Desai
- Around 34 participants attended the alumni talk including students, faculties and alumni and learned about the recent trends and opportunities in AI, ML, VLSI & Automotive domain.

## Objective of the event:

The objective of the Alumni Talk Series - ‘Abhivyakti’ is to organize interactions on a digital platform among current students and alumni on career opportunities and emerging trends of industry in different domains. The experiences and knowledge of alumni who have excelled in performance in their respective fields can motivate and guide the current students of GIT.

## Brief Profile of the speaker:

Akash Verma has pursued his Bachelor’s degree of engineering in Electronics and Communication from Gandhinagar Institute of Technology during 2014-2018. He was brilliant in his academic performance and received Devang Mehta IT Award consecutively for 4 years. In 3<sup>rd</sup> semester, he was felicitated by GTU-CiC3 club for an excellent project presentation on “Intelligent Vision” during Digital India Week 2015 celebrations. In 4<sup>th</sup> semester, he assisted GTU Robocon team as Jr Software developer and devised machine learning based projects on open CV & python during 2015-16. In 6<sup>th</sup> semester, he initiated, lead and implemented IoT based classified software solutions for critical business processes in a start-up company named Vitgel Business Solution during 2016-2017. In 7<sup>th</sup> semester, he joined Indicus Technology for 6 months internship and learned various ASIC, FPGA and SOC design technologies. After completing BE, he got placement at eInfochips as an ASIC Physical Design Engineer. He has project experience of Intel’s Thunderbolt technology working on 7nm SoC. He also worked on different projects of nVidia and Juniper Network. Presently, he is working with Facebook as an ASIC Design engineer.

## Activities during the event:

Under the banner of Gandhinagar Institute of Technology Alumni Association (GITAA) & SSIP, GIT invited his Alumnus Mr. Akash Verma of batch 2014-2018 from Electronics & Communication Engineering department for an expert session on “Latest Advancements in AI, ML, VLSI & Automotive Realm”.

The expert session was held on 14-06-2020 during 11:30 to 12:30 pm on Google Meet platform. Prof Megha Desai gave an introductory speech about the speaker of the session Mr Akash Verma. Around 34 students, alumni and faculties participated in this expert talk on recent trends in electronics field. Alumni Mr Akash Verma inspired the students with the story of his journey in electronics and communication field. He explained about the opportunities for an EC engineer in the fields of AI, ML & VLSI design, embedded system, communication, computer vision, image processing, networking, digital signal processing and automation. He gave the details of the methodology and application levels in VLSI technology and working of image processing tools through examples.

He guided the students about the necessary skill sets to be developed for becoming industry ready like various programming languages- Verilog, Python, Perl, C, C++ and tools like-Xilinx, Quartus, Labview, MATLAB etc. He gave a glimpse of emerging technologies in the fields of Artificial Intelligence, Machine Learning, 5G, IOT, Automation and Quantum Computing. He solved many queries of the students regarding career choices and job opportunities in EC domain. The session was concluded with vote of thanks given by faculties of the EC department.

**Outcomes of the event:**

The Alumni Talk has facilitated interactions of students with alumni for career development & skills needed for grasping better job opportunities. The students were very enthusiastic to learn about present industry scenario in AI, ML, VLSI field.

**Feedback from students:**

The whole webinar was interesting and I learned about basics of artificial intelligence and machine learning.

- Sreeraj Tharakan  
4<sup>th</sup> BE (EC) (180120111010)

The webinar was very informative and I am thankful to the alumnus for sharing his experiences.

- Krutarth Daxeshbhai Patel  
4<sup>th</sup> BE (CE) (180120107117)

**Feedback from faculty:**

The session was wonderful and interactive specially Q/A. Our current students can learn and gain so many things from expertise of alumni which would guide them to focus on enhancing their technical skills.

- Prof. Mitul Maniar  
Asst. Prof, EC Department

**Feedback from Alumni Expert:**

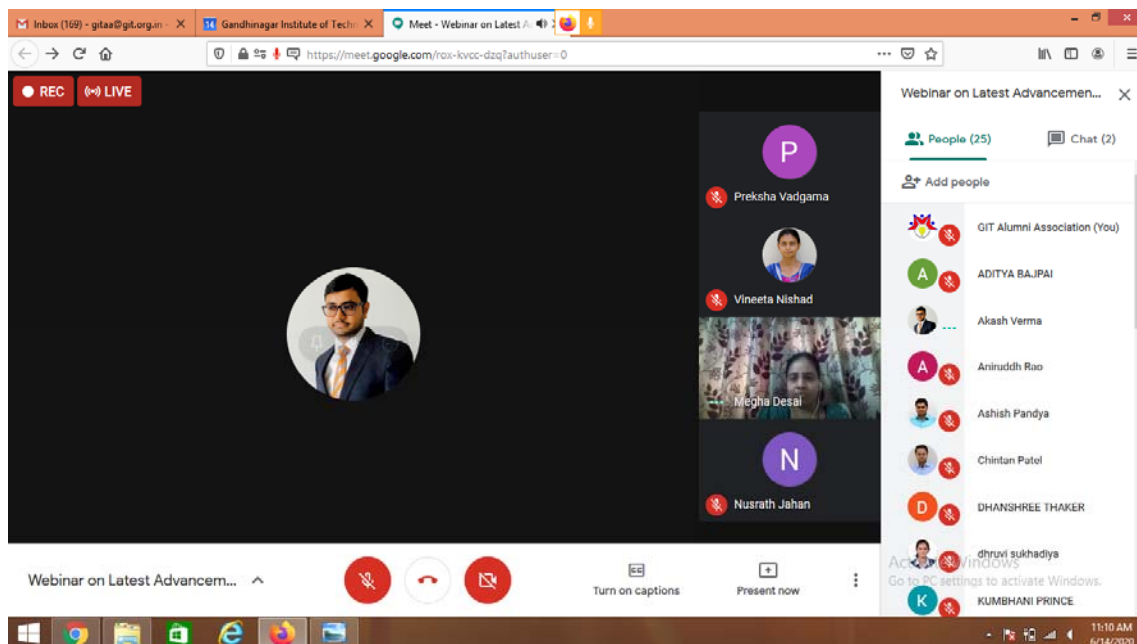
I am thankful to GIT for inviting me to share my experiences and meet my faculties again. I hope that students will get inspired from my journey and become more focused towards their career goal and learning new skills for becoming industry ready. It was such a good interaction with the young engineers. I would be very happy to help the students in future also.

- Mr. Akash Verma  
ASIC Design Engineer, Facebook  
Contact No: 7984730321  
Email: akash.verma98@yahoo.com

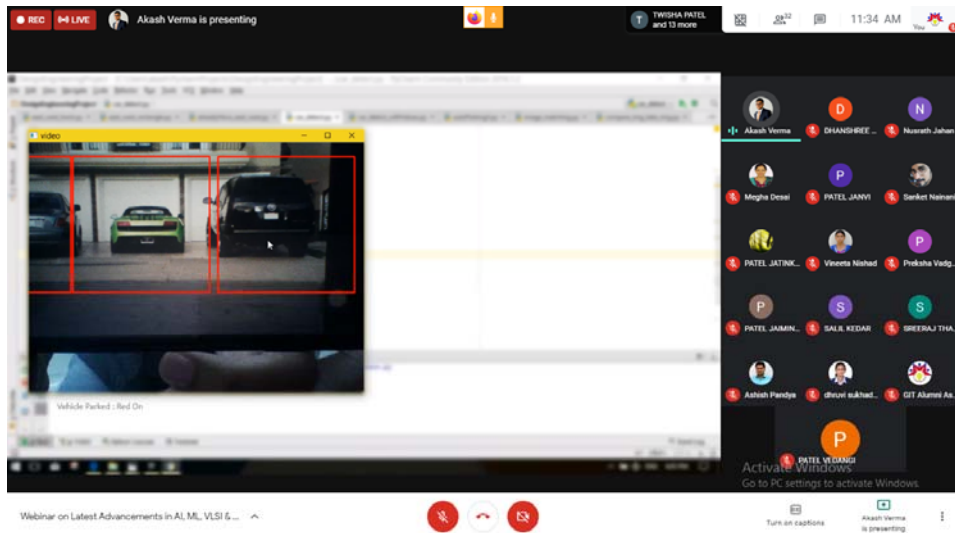
**Photo Gallery:**



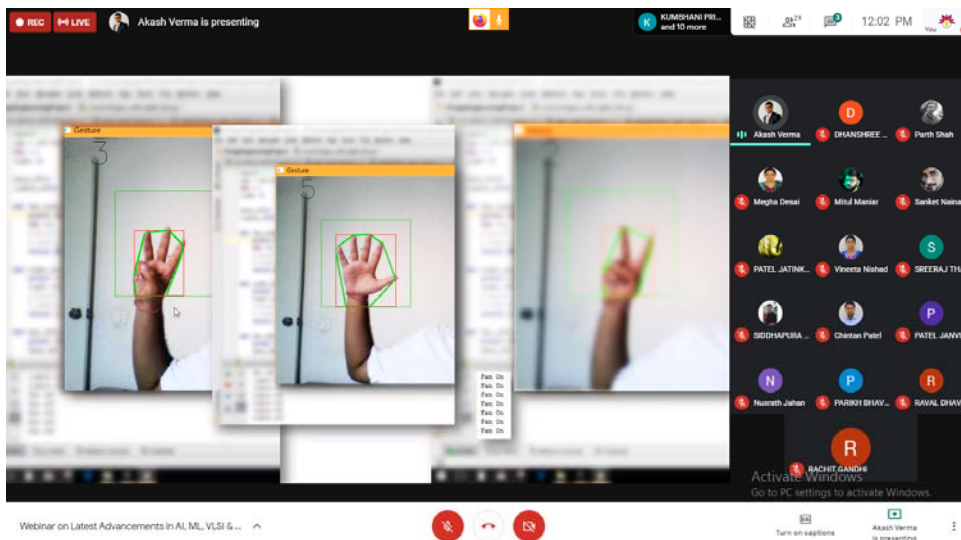
Social Media Creative of the webinar



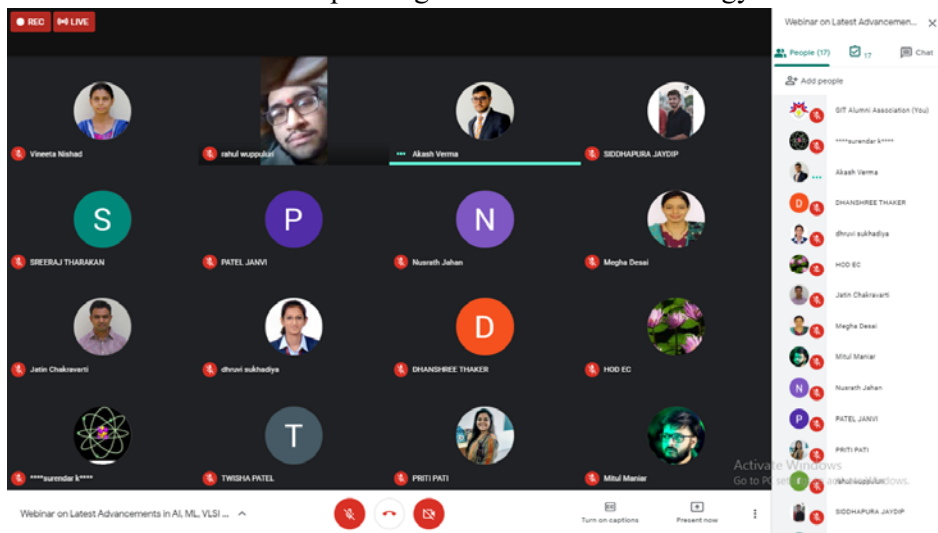
Alumni explaining about VLSI technology



Alumni explaining about image processing project



Alumni explaining methods of AI technology



Alumni interacting with the audience and solving their queries

